

## Patent Abstracts of Japan

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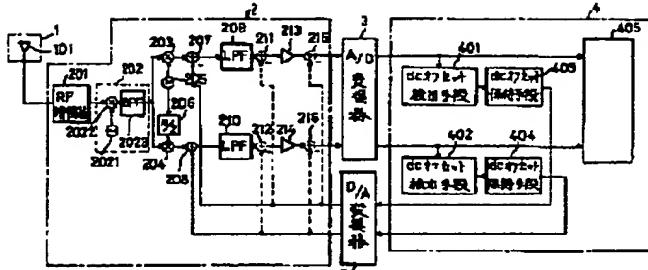
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TITLE : RECEIVER PROVIDED WITH DC  
 OFFSET REMOVAL FUNCTION AND  
 RADIO COMMUNICATION SYSTEM  
 USING THE SAME



ABSTRACT : PROBLEM TO BE SOLVED: To overcome the deterioration of a reception characteristic caused by a DC offset generated when a received signal is processed in an analog signal processing circuit and to realize a satisfactory call.

SOLUTION: An analog signal processing circuit 2 processing an analog signal inputted from a signal input part 1 and generating the unnecessary DC offset in a signal route and a digital signal processing circuit 4 which A/D- converts 3 the output and processes a converted digital signal are provided for a receiver. The digital signal processing circuit is provided with offset detection means 401 and 402 for detecting the offset and means 403 and 404 for holding the detected DC offset. A D/A converter converts a part of the DC offset which is held into an analog signal, and a second DC offset correction means provided for the analog signal processing circuit reduces the DC offset and removes the DC offset generated in the analog signal processing circuit.

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